

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Robert Furst et al.                      Art Unit : 2814  
Serial No. : 10/501,209                              Examiner : Nathan W. Ha  
Filed : January 6, 2005                              Conf. No. : 3577  
Title : PHOTODIODE ARRAY AND METHOD FOR ESTABLISHING A LINK  
BETWEEN A FIRST SEMICONDUCTOR ELEMENT AND A SECOND  
SEMICONDUCTOR ELEMENT

**MAIL STOP RCE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

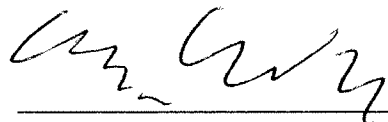
INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the references listed on the attached PTO-1449 form. Under 37 C.F.R. § 1.98 (a)(2)(ii), only copies of foreign patent documents and/or non-patent literature are enclosed. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,

Date: 2/19/08

  
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I hereby certify that this paper was filed with the Patent and Trademark Office using the EFS-WEB system on this date: February 19, 2008